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March 15, 2001

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner of Patents, Washington, D.C. on

3/15/01
April N. Williams
April N. Williams
Date of Signature 3/15/01

Commissioner for Patents
Washington, D.C. 20231

Re: U.S. Patent Application Serial No. 09/723,356 for METHODS AND SYSTEMS FOR REACTIVELY COMPENSATING MAGNETIC CURRENT LOOPS
Our File No. 421/31

Sir:

Please find enclosed in connection with the subject U.S. patent application the following documents:

1. Information Disclosure Statement (2 pages);
2. Form PTO/SB/08A (1 page), in duplicate;
3. Copies of cited references (4 references);
4. A return-receipt postcard to be returned to us with the U.S. Patent and Trademark Office filing stamp thereon.

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Respectfully submitted,

JENKINS & WILSON, P.A.

Gregory A. Hunt
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GAH/anw
Enclosures



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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Vicci et al.

Group Art Unit: 2632

Serial No.: 09/723,356

Examiner: Not Assigned

Filed: November 27, 2000

Docket No.: 421/31

For: METHODS AND SYSTEMS FOR REACTIVELY COMPENSATING
MAGNETIC CURRENT LOOPS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

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Sir:

In accordance with 37 C.F.R. 1.56, 1.97, and 1.98, applicants' undersigned attorney brings to the attention of the Patent and Trademark Office the following references. Copies of the references as well as Form PTO/SB/08A are attached hereto. This is not to be construed as a representation that a search has been made or that a reference is relevant merely because cited.

U.S. Patent No. 5,198,746 to Gyugyi et al. discloses a transmission line dynamic impedance compensation system.

U.S. Patent No. 4,757,434 to Kawabata et al. discloses a control circuit used for a power conversion apparatus.

U.S. Patent No. 4,087,646 to Brolin et al. discloses a dual mode telephone subscriber loop current detector.

U.S. Patent No. 5,946,178 to Bijlenga discloses a protective circuit for series-connected power semiconductors.

Early passage of the subject application to issue is earnestly solicited.

Although it is believed that no fee is due, the Commissioner is hereby authorized to charge any deficiencies of payment associated with the filing of this Information Disclosure Statement to Deposit Account No. 50-0426.

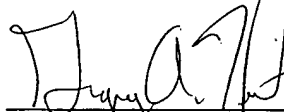
Respectfully submitted,

JENKINS & WILSON, P.A.

Date:

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By:



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Enclosures